Typical Features

- ◆ Fixed input voltage, isolated & unregulated, output power 1W
- ◆ Efficiency up to 86% (Typ.)
- ◆ Mini SMD package, standard pin-out alignment
- ◆ Isolation voltage 1500VDC
- ◆ Operating temperature from -40℃ to +105℃
- ◆ Low no load input current
- ◆ Plastic case, flame class UL94 V-0

Test conditions: Unless otherwise specified, all parameter values had been tested at nominal input voltage, pure resistive rated load, and at room temperature 25 ℃.

Application Filed

This series of converters can be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Certificate	Part No.	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current (mA)Typ. @Nominal voltage		Max. Capacitive Load	Ripple & Noise 20MHz (mVp-p)	Efficiency (%) @full load/nom. voltage	
		Nom.	Range	Vo (VDC)	Io (mA) Max / Min	Full load	No Load	uF	Max/Typ	Min	Typ
-	NN1-05D3V3ANT	5	4.5 - 5.5	±3.3	±152/±15	250	8	1200	100/80	74	78
-	NN1-05D05ANT			±5	±100/±10	230	8	1200	100/80	81	84
-	NN1-05D09ANT			±9	±55/±6	228	10	1200	100/80	81	84
-	NN1-05D12ANT			±12	±42/±4	226	14	470	100/80	81	84
-	NN1-05D15ANT			±15	±33/±3	230	16	330	100/80	80	83
-	NN1-05D24ANT			±24	±21/±2	245	20	330	100/80	80	83
-	NN1-12D05ANT	12	10.8 - 13.2	±5	±100/±10	98	8	1200	100/80	81	84
-	NN1-12D09ANT			±9	±56/±6	96	8	1000	100/80	81	84
-	NN1-12D12ANT			±12	±42/±4	96	8	470	100/80	82	85
-	NN1-12D15ANT			±15	±33/±3	92	9	470	100/80	83	86
-	NN1-24D05ANT	24	21.6 - 26.4	±5	±100/±10	48	8	1200	100/80	81	84
-	NN1-24D09ANT			±9	±56/±6	46	8	1000	100/80	81	84
-	NN1-24D12ANT			±12	±42/±4	46	8	470	100/80	82	85
-	NN1-24D15ANT			±15	±33/±3	46	8	470	100/80	83	86

Note: The Ripple & Noise is tested by the twisted pair method.

Input Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Input inrush voltage (1 sec. max.)	3.3Vdc Input	-0.7	--	7	Vdc
	5Vdc Input	-0.7	--	9	
	9Vdc Input	-0.7	--	12	
	12Vdc Input	-0.7	--	18	
	15Vdc Input	-0.7	--	21	
	24Vdc Input	-0.7	--	30	
Input filter type	Capacitor Filter				
Hot plug	Unavailable				

Output Specifications

Item	Operating Condition		Min.	Typ.	Max.	Unit
Output power			0.1	--	1	W
Output voltage accuracy	Please refer to the output voltage deviation graphs (Figure 1)					
Load regulation	10%-100% load	3.3Vdc output	-	15	20	%
		Others output	-	10	15	
Line regulation	Input voltage change ±1%	3.3Vdc output	-	-	1.5	--
		Others output	-	-	1.2	
Temperature drift coefficient	Full load		-	-	±0.03	%/℃
Short circuit protection	Continuous, Self-recovery					

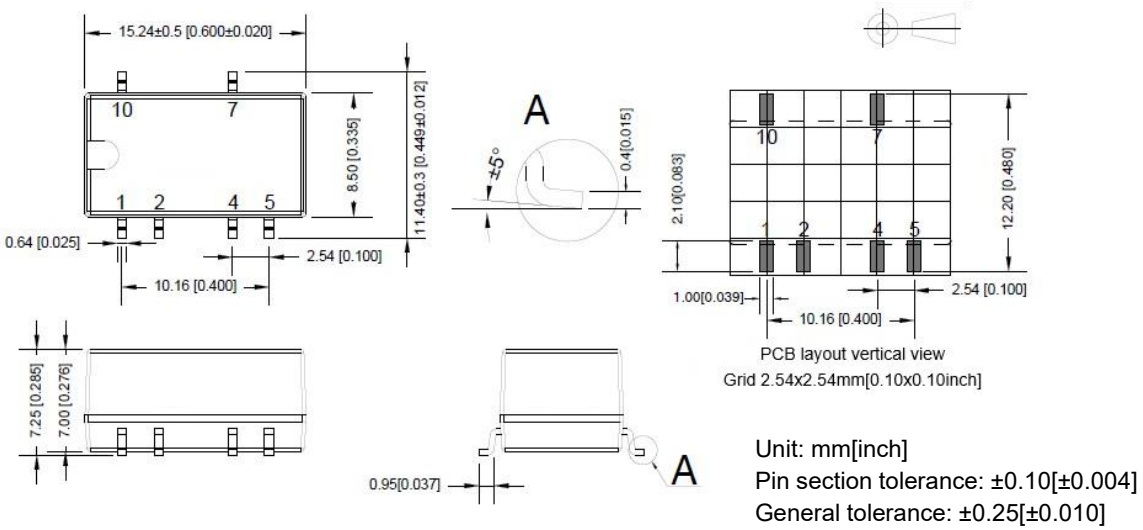
General Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Switching frequency	Nominal input voltage, full load	--	260	--	KHz
Operating temperature	Refer to the temperature derating graph (Figure 2)	-40	--	+105	℃
Storage temperature		-55	--	+125	
Case temperature rise	Operating at Ta =25℃	--	30°	--	
Pin soldering temperature	1.5mm from the case, 10S	--	--	300	
Reflow temperature	Peak temperature Tc≤250℃, the maximum time 60S above 217℃				
Relative humidity	No condensing	5	--	95	%RH
Isolation voltage	Input/Output, test 1min, leakage current <1mA	1500	--	--	VDC
Insulation resistance	Input/Output, @ 500VDC	1000	--	--	MΩ
Isolation capacitor	Input/Output,100KHz/0.1V	--	20	--	pF
MTBF	MIL-HDBK-217F@25℃	3500	--	--	K hours
Case material	Plastic in Black, flame class UL94 V-0				
Unit weight	1.4 g (Typ.)				
Cooling method	Natural air				
Unit dimensions	L x W x H	15.24X11.40X7.25 mm		0.600 × 0.449 × 0.285 inch	

EMC Performance

EMI	CE	CISPR32/EN55032 CLASS B (with the Recommended EMC Circuit)
	RE	CISPR32/EN55032 CLASS B (with the Recommended EMC Circuit)
EMS	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±6kV perf. Criteria B

Mechanical Dimensions



Unit: mm[inch]
Pin section tolerance: ±0.10[±0.004]
General tolerance: ±0.25[±0.010]

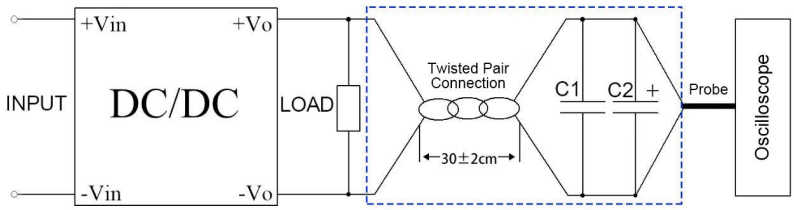
Pin-out Function Description

Pin No.	1	2	4	5	7	10
Function	GND	+Vin	COM	-Vo	+Vo	NC

Note 1: Please take the pin definition on the product label as the right one if it is different than the data sheet description.

Note 2: Pin 10 NC means No Connection with any external circuit.

Ripple & Noise Test Instruction (Twisted Pair Method, 20MHz Bandwidth)



1. The Ripple & noise test needs 12# twisted pair cables, an oscilloscope which should be set at the Sample Mode, bandwidth 20MHz. 100M bandwidth probe with cap and ground removed. C1(0.1uF) polypropylene capacitor and C2(10uF) high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes and one side of the twisted pair.
2. The power supply output connects to the load by the cables. The other side of the twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the polarity of the output and the oscilloscope probe should not be reversed. The test can be start after input power on.
3. It is recommended to connect a ⩾5% load or a high-frequency low resistance E-cap(⩾100uF) load at output to avoid the output ripple increasing.

Product Characteristics Graphs

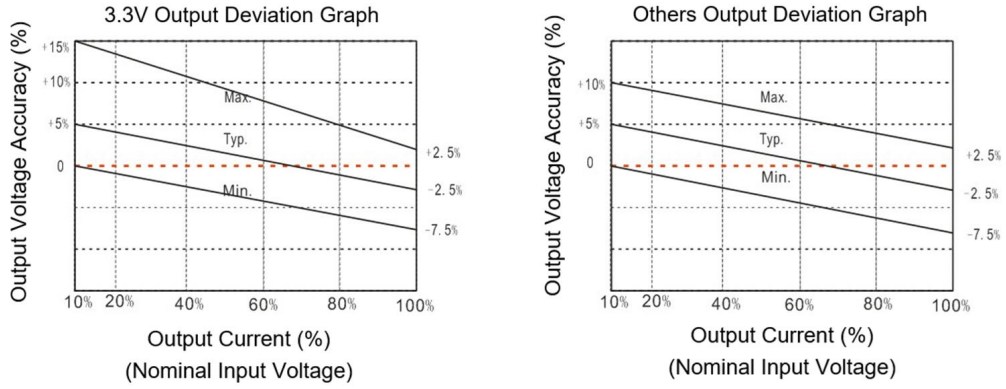


Figure 1

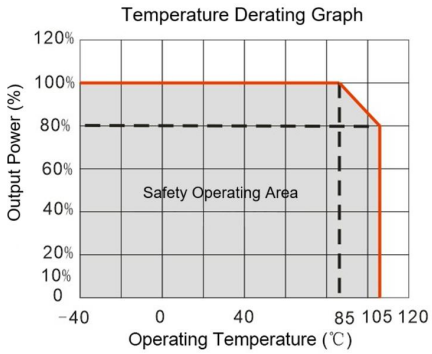


Figure 2

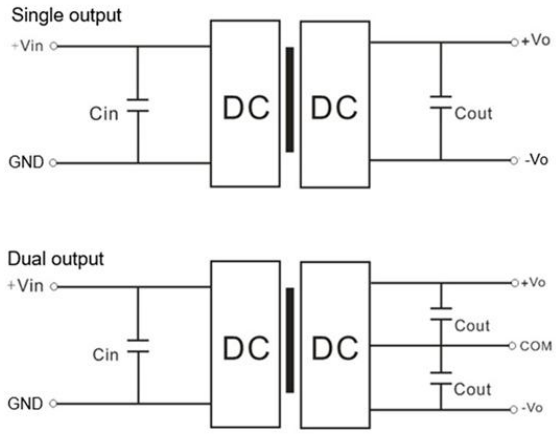
Recommended Circuits for Application

1. Output load requirements

The maximum capacitive load of the product was tested at the Rated full load. The converter may not start or be damaged if the output capacitor exceeds this value.

2. Typical application circuit

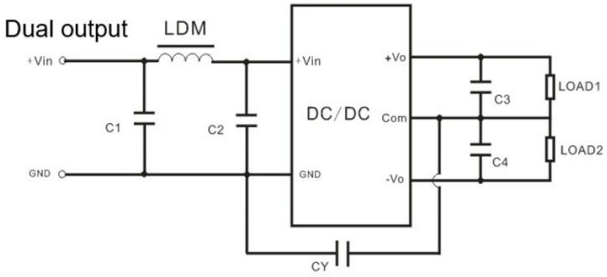
To effectively decrease the input and output ripple and noise, a capacitor filter should be connected at the input and output, the application circuits diagrams are shown in the figure below. The suitable filter capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.



Recommended Capacitive Load Value Table (Table 1)

Vin (Vdc)	Cin	Single Vout (Vdc)	Cout (μF)	Dual Vout (Vdc)	Cout (μF)
5	10μF/16V	3.3	10μF/16V	±3.3	4.7μF/16V
12	2.2μF/25V	5	10μF/16V	±5	4.7μF/16V
15	2.2μF/25V	9	2.2μF/25V	±9	2.2μF/25V
24	1μF/50V	12	2.2μF/25V	±12	1μF/25V
--	--	15	1μF/25V	±15	1μF/25V
--	--	24	1μF/50V	±24	0.47μF/50V

3. Recommended EMC Circuit

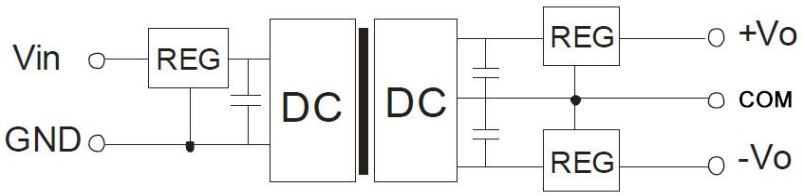


Input voltage		5VDC	12/15/24VDC
EMI	C1/C2	4.7μF/16V	4.7μF/50V
	CY	270 pF	270 pF
	C3/C4	Refer to Cout in Table 1	
	LDM	6.8μH	6.8μH

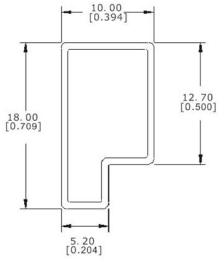
4. Output voltage regulation and over voltage protection

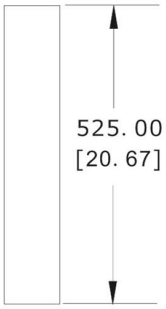
The simple solution to achieve the output voltage regulated, over voltage and over current protections is to connect a linear regulator with overheat protection at input or output, and a capacitor filter connected in parallel as below circuit. Filter capacitive value recommended see table 1, Linear regulator should be chosen according to the actual voltage & current for operating. Or Aipu NW series products are recommended instead.

Dual output



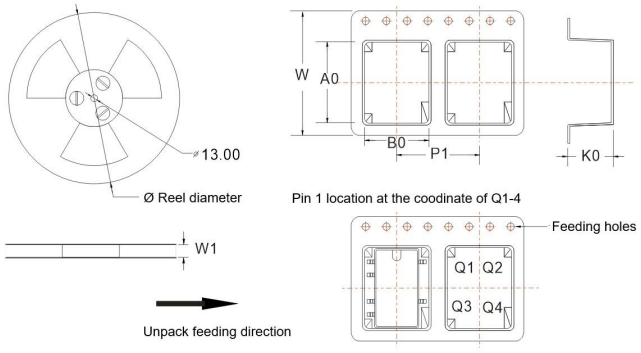
Packing Information





Note:
Unit: mm[inch]
General tolerance: ±1.50[±0.059]
Packing QTY: 33pcs/Tube
Packing QTY: 2640pcs/Carton
Tube size: 525x18x10mm
Carton size: 542x110x155mm

Tube packing



Part No.	Packaging Type	Pin	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W1 (mm)	Pin1 Location
NNX...DXXA(3)NT	SMD	6	500	330.0	24.5	15.65	12.05	8.0	16.0	24.0	Q1

Reel packing (500pcs per Reel)

Application Notice

1.This series of products cannot be used by connecting in parallel, and do not support hot-plugging.

2.The product performance cannot be guaranteed if it works at a lower load than the minimum load condition.

3. All values or indicators on this data sheet have been tested based on Aipupower test specifications.

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